## **SIEMENS**

## Data sheet

## 6ES7314-6BH04-0AB0



SIMATIC S7-300, CPU 314C-2 PTP Compact CPU with MPI, 24 DI/16 DO, 4 AI, 2 AO, 1 Pt100, 4 high-speed counters (60 kHz), integrated interface RS485, Integr. power supply 24 V DC, work memory 192 KB, Front connector (2x 40-pole) and Micro Memory Card required

General information	
HW functional status	01
Firmware version	V3.3
Engineering with	
Programming package	STEP 7 as of V5.5 + SP1 or STEP 7 V5.3 + SP2 or higher with HSP 204
Supply voltage	
Rated value (DC)	24 V
permissible range, lower limit (DC)	19.2 V
permissible range, upper limit (DC)	28.8 V
external protection for power supply lines	Miniature circuit breaker, type C; min. 2 A; miniature circuit
(recommendation)	breaker type B, min. 4 A
Mains buffering	
<ul> <li>Mains/voltage failure stored energy time</li> </ul>	5 ms
• Repeat rate, min.	1 s
Load voltage L+	
Digital inputs	
— Rated value (DC)	24 V
<ul> <li>Reverse polarity protection</li> </ul>	Yes

Digital outputs	
— Rated value (DC)	24 V
Reverse polarity protection	No
Input current Current consumption (rated value)	660 mA
Current consumption (in no-load operation), typ.	150 mA
Inrush current, typ.	5 A
I²t	0.7 A <sup>2</sup> ·s
Digital inputs	o / C
• from load voltage L+ (without load), max.	80 mA
Digital outputs	
• from load voltage L+, max.	50 mA
_	
Power loss	
Power loss, typ.	13 W
Memory	
Work memory	
• integrated	192 kbyte
• expandable	No
<ul> <li>Size of retentive memory for retentive data</li> </ul>	64 kbyte
blocks	
Load memory	
• Plug-in (MMC)	Yes
<ul><li>Plug-in (MMC), max.</li></ul>	8 Mbyte
	40
<ul> <li>Data management on MMC (after last</li> </ul>	10 y
programming), min.	10 y
•	
programming), min.  Backup  • present	Yes; Guaranteed by MMC (maintenance-free)
programming), min.  Backup	
programming), min.  Backup  • present	Yes; Guaranteed by MMC (maintenance-free)
programming), min.  Backup  • present  • without battery	Yes; Guaranteed by MMC (maintenance-free)
programming), min.  Backup  • present  • without battery  CPU processing times	Yes; Guaranteed by MMC (maintenance-free) Yes; Program and data
programming), min.  Backup  • present  • without battery  CPU processing times for bit operations, typ.	Yes; Guaranteed by MMC (maintenance-free) Yes; Program and data  0.06 µs
programming), min.  Backup  • present  • without battery  CPU processing times for bit operations, typ.  for word operations, typ.	Yes; Guaranteed by MMC (maintenance-free) Yes; Program and data  0.06 µs 0.12 µs
programming), min.  Backup  • present  • without battery  CPU processing times  for bit operations, typ.  for word operations, typ.  for fixed point arithmetic, typ.	Yes; Guaranteed by MMC (maintenance-free) Yes; Program and data  0.06    0.12    0.16    0.16    0.16   0.18   0.19   0.19   0.10   0.1
programming), min.  Backup  • present  • without battery  CPU processing times for bit operations, typ. for word operations, typ. for fixed point arithmetic, typ. for floating point arithmetic, typ.	Yes; Guaranteed by MMC (maintenance-free) Yes; Program and data  0.06    0.12    0.16    0.16    0.16   0.18   0.19   0.19   0.10   0.1
programming), min.  Backup  • present  • without battery  CPU processing times for bit operations, typ. for word operations, typ. for fixed point arithmetic, typ. for floating point arithmetic, typ.  CPU-blocks	Yes; Guaranteed by MMC (maintenance-free) Yes; Program and data  0.06    0.12    0.16    0.59    0.59    0.59  0.59
programming), min.  Backup  • present  • without battery  CPU processing times for bit operations, typ. for word operations, typ. for fixed point arithmetic, typ. for floating point arithmetic, typ.  CPU-blocks	Yes; Guaranteed by MMC (maintenance-free) Yes; Program and data  0.06 µs 0.12 µs 0.16 µs 0.59 µs  1 024; (DBs, FCs, FBs); the maximum number of loadable blocks can be reduced by the MMC used.
programming), min.  Backup  • present  • without battery  CPU processing times for bit operations, typ. for word operations, typ. for fixed point arithmetic, typ. for floating point arithmetic, typ.  CPU-blocks Number of blocks (total)	Yes; Guaranteed by MMC (maintenance-free) Yes; Program and data  0.06 µs 0.12 µs 0.16 µs 0.59 µs  1 024; (DBs, FCs, FBs); the maximum number of loadable blocks can be reduced by the MMC used.
programming), min.  Backup  • present  • without battery  CPU processing times for bit operations, typ. for word operations, typ. for fixed point arithmetic, typ. for floating point arithmetic, typ.  CPU-blocks Number of blocks (total)	Yes; Guaranteed by MMC (maintenance-free) Yes; Program and data  0.06 µs 0.12 µs 0.16 µs 0.59 µs  1 024; (DBs, FCs, FBs); the maximum number of loadable blocks can be reduced by the MMC used.
programming), min.  Backup  • present  • without battery  CPU processing times for bit operations, typ. for word operations, typ. for fixed point arithmetic, typ. for floating point arithmetic, typ.  CPU-blocks Number of blocks (total)  DB  • Number, max.	Yes; Guaranteed by MMC (maintenance-free) Yes; Program and data  0.06 µs 0.12 µs 0.16 µs 0.59 µs  1 024; (DBs, FCs, FBs); the maximum number of loadable blocks can be reduced by the MMC used.

• Size, max.	64 kbyte
FC	
Number, max.	1 024; Number range: 0 to 7999
• Size, max.	64 kbyte
OB	
Description	see instruction list
• Size, max.	64 kbyte
<ul> <li>Number of free cycle OBs</li> </ul>	1; OB 1
<ul> <li>Number of time alarm OBs</li> </ul>	1; OB 10
<ul> <li>Number of delay alarm OBs</li> </ul>	2; OB 20, 21
<ul> <li>Number of cyclic interrupt OBs</li> </ul>	4; OB 32, 33, 34, 35
<ul> <li>Number of process alarm OBs</li> </ul>	1; OB 40
<ul> <li>Number of startup OBs</li> </ul>	1; OB 100
<ul> <li>Number of asynchronous error OBs</li> </ul>	4; OB 80, 82, 85, 87
<ul> <li>Number of synchronous error OBs</li> </ul>	2; OB 121, 122
Nesting depth	
• per priority class	16
<ul> <li>additional within an error OB</li> </ul>	4
Countary timers and their retentivity	
Counters, timers and their retentivity  S7 counter	
• Number	256
Retentivity	
— adjustable	Yes
— lower limit	0
— upper limit	255
— preset	Z 0 to Z 7
Counting range	
— lower limit	0
— upper limit	999
IEC counter	
• present	Yes
• Type	SFB
• Number	Unlimited (limited only by RAM capacity)
S7 times	
• Number	256
Retentivity	
— adjustable	Yes
— lower limit	0
— upper limit	255
— preset	No retentivity
Time range	

— lower limit	10 ms
— upper limit	9 990 s
IEC timer	
• present	Yes
• Type	SFB
• Number	Unlimited (limited only by RAM capacity)
	, , , , , , , , , , , , , , , , , , , ,
Data areas and their retentivity	
retentive data area in total	all, max. 64 KB
Flag	0501
Number, max.	256 byte
Retentivity available	Yes; MB 0 to MB 255
<ul> <li>Retentivity preset</li> </ul>	MB 0 to MB 15
Number of clock memories	8; 1 memory byte
Data blocks	
<ul> <li>Retentivity adjustable</li> </ul>	Yes; via non-retain property on DB
<ul><li>Retentivity preset</li></ul>	Yes
Local data	
<ul><li>per priority class, max.</li></ul>	32 kbyte; Max. 2048 bytes per block
Address area	
I/O address area	
• Inputs	1 024 byte
Outputs	1 024 byte
of which distributed	
— Inputs	none
— Outputs	none
Process image	
• Inputs	1 024 byte
Outputs	1 024 byte
Inputs, adjustable	1 024 byte
Outputs, adjustable	1 024 byte
• Inputs, default	128 byte
Outputs, default	128 byte
Default addresses of the integrated channels	
— Digital inputs	124.0 to 126.7
— Digital outputs	124.0 to 125.7
— Analog inputs	752 to 761
— Analog outputs	752 to 755
Digital channels	
• Inputs	1 016
— of which central	1 016
Outputs	1 008
Carpaio	

Analog channels  Injusts 253 — of which central 253  Outputs 250 — of which central 250  Hardware configuration  Number of expansion units, max. 3  Number of DP masters  Integrated none Inte	— of which central	1 008
Outputs 250 Outputs 250 Outputs 250  Hardware configuration Number of expansion units, max. 3 Number of DP masters  integrated none 4 Via CP Number of operable FMs and CPs (recommended)  FM 8 OCP, PIP 8 OCP, LAN 10 Rack  Racks, max. 4 Modules per rack, max. 4 Modules per rack, max. 4 Hardware clock (real-time) Yes Face Ardware clock (real-time) Yes Backup time 6 kw; At 40 °C ambient temperature 10 s; Typ: 2 s Behavior of the clock following POWER-ON Behavior of the clock following expiry of backup period  Operating hours counter  Number 1 Number 1 Number 2 Number 3 Number 1 Number 3 Number 3 Number 4 Number 4 Number 5 Number 6 Number 9 Number 1 Number 9 Number 1 Number 1 Number 1 Number 1 Number 1 Number 1 Number 2 Number 3 Number 3 Number 4 Number 4 Number 5 Number 5 Number 6 Number 6 Number 7 Number 7 Number 7 Number 8 Number 8 Number 9 Number 1 Number 9 Number 1 Number 1 Number 1 Number 9 Number 1 Number 2 Number 3 Number 4 Number 4 Number 5 Number 5 Number 6 Number 7 Number 7 Number 7 Number 7 Number 8 Number 8 Number 8 Number 9 Number 9 Number 9 Number 9 Number 1 Number 9 Number 1 Number 9 Number 1 Number 9 Number 1 Number	Analog channels	
Outputs Of which central  Plandware configuration  Number of expansion units, max.  Number of DP masters  integrated  integra	• Inputs	253
Hardware configuration  Number of expansion units, max.  Number of DP masters  integrated  integrated  ovia CP  Number of operable FMs and CPs (recommended)  FM  CP, PrP  8  CP, LAN  10  Rack  Racks, max.  Modules per rack, max.  Modules per rack, max.  Hardware clock (real-time)  retentive and synchronizable  Backup time  Deviation per day, max.  Behavior of the clock following POWER-ON  Behavior of the clock following expiry of backup period  Operating hours counter  Number  Numb	— of which central	253
Hardware configuration  Number of expansion units, max.  integrated via CP  Number of operable FMs and CPs (recommended)  FM CP, PtP B CP, LAN 10  Rack  Racks, max. Modules per rack, max. Modules per rack, max.  Hardware clock (real-time) retentive and synchronizable Behavior of the clock following expiry of backup period  Departing hours counter  Number Number Range of values O to 2^31 hours (when using SFC 101) Fretentive Pess Supported Yes Olock Pass Supported Yes Supported Yes Number Ves Supported Yes No	Outputs	250
Number of expansion units, max.  Number of DP masters  integrated via CP  Number of operable FMs and CPs (recommended)  FM CP, PIP 8 CP, LAN 10  Rack  Racks, max. Modules per rack, max.  Hardware clock (real-time) retentive and synchronizable Behavior of the clock following expiry of backup period  Clock  Period of the clock following expiry of backup period  Operating hours counter  Number Number Range of values Granularity retentive Supported Yes Oto to 2^31 hours (when using SFC 101) Yes Oto to MPI, slave Yes No Digital inputs	— of which central	250
Number of expansion units, max.  Number of DP masters  integrated via CP  Number of operable FMs and CPs (recommended)  FM CP, PIP 8 CP, LAN 10  Rack  Racks, max. Modules per rack, max.  Hardware clock (real-time) retentive and synchronizable Behavior of the clock following expiry of backup period  Clock  Period of the clock following expiry of backup period  Operating hours counter  Number Number Range of values Granularity retentive Supported Yes Oto to 2^31 hours (when using SFC 101) Yes Oto to MPI, slave Yes No Digital inputs	Handrigan and Complian	
Number of DP masters		and the second s
integrated via CP  Number of operable FMs and CPs (recommended)  FM CP, PtP 8 CP, LAN 10  Rack  Racks, max. Modules per rack, max. Modules per rack, max.  Hardware clock (real-time) retentive and synchronizable Backup time Deviation per day, max. Behavior of the clock following POWER-ON Behavior of the clock following expiry of backup period  Operating hours counter  Number Number Number Range of values Granularity retentive Full Mumber Range of values Granularity retentive Full Mumber Summer		
via CP  Number of operable FMs and CPs (recommended)  FM     CP, PM     Reck     Racks, max.     Modules per rack, max.  Modules per rack, max.  Hardware clock (real-time)     retentive and synchronizable     Backup time     Behavior of the clock following POWER-ON     Behavior of the clock following expiry of backup period  Operating hours counter  Number Number Range of values     Range of values     Range of values     Range of values     Supported     Supported     Supported     Ves     Supported     Ves     Supported     Ves     Ves     Supported     Ves     Ves     Ves     Ves     No     Digital inputs		none
Number of operable FMs and CPs (recommended)  • FM  • CP, PtP  • CP, LAN  10  Rack  • Racks, max. • Modules per rack, max.  • Modules per rack, max.  • Hardware clock (real-time) • retentive and synchronizable • Backup time • Deviation per day, max.  • Behavior of the clock following POWER-ON • Behavior of the clock following expiry of backup period  Operating hours counter  • Number • Number • Number • Number 1 • Number/Number range • Range of values • Granularity • retentive • Synchronization • supported • Synchronization • supported • to MPI, master • to MPI, slave • in AS, master • in AS, slave  Digital inputs		
FM CP, PtP CP, LAN  10  Rack Rack Racks, max. Modules per rack, max. Modules per rack, max.  Firme of day  Clock  Hardware clock (real-time) retentive and synchronizable Behavior of the clock following POWER-ON Behavior of the clock following expiry of backup period  Number  Number Number Number Range of values Granularity Tetentive Supported To MPI, master To MPI, slave To MC, Land To Rack 3 max. 7   He see that 3 max. 7  Yes  Wes Wes Wes Wes Wes Wes Wes Wes Wes		
CP, PIP CP, LAN CP, L		8
CP, LAN  Rack  Racks, max.  Modules per rack, max.  Modules per rack, max.  Modules per rack, max.  Per dentive and synchronizable  Backup time  Deviation per day, max.  Behavior of the clock following POWER-ON  Behavior of the clock following expiry of backup period  Perating hours counter  Number  Number  Range of values  Granularity  retentive  Pes; Must be restarted at each restart  Clock synchronization  Supported  Supported  Supported  Yes  In AS, master  Yes  In AS, slave  Pes  A modules per rack, max.  4  8; In rack 3 max. 7  Clock a max. 7  Yes  Clock combinues tumping after POWER OFF  Clock continues running after POWER OFF  Clock continues to run with the time at which the power failure occurred  Oto 2^31 hours (when using SFC 101)  Yes; Must be restarted at each restart  Clock synchronization  Yes  No  Digital inputs		
Rack  Rack  Racks, max.  Modules per rack, max.  Rack  Racks, max.  Ra		
Modules per rack, max.      Ri, In rack 3 max. 7  Time of day  Clock      Hardware clock (real-time)     retentive and synchronizable     Backup time     Deviation per day, max.     Behavior of the clock following POWER-ON     Behavior of the clock following expiry of backup period  Operating hours counter      Number     Number/Number range     Range of values     Granularity     retentive  Clock synchronization      supported     Yes     to MPI, master     in AS, master     in AS, slave  No  Yes  Yes  Yes  In rack 3 max. 7  Yes   Yes  Yes  No  Yes  No  Yes  No  No  Yes  No  No  Yes  No  No  Yes  No  No  No  No  No  No  No  Yes  No  No  No  No  No  No  No  No  No  N		
Modules per rack, max.  8; In rack 3 max. 7  Time of day  Clock  Hardware clock (real-time) retentive and synchronizable Backup time Deviation per day, max. Behavior of the clock following POWER-ON Behavior of the clock following expiry of backup period  Operating hours counter  Number Number Range of values Range of values Granularity retentive  Clock synchronization  Supported Yes To MPI, slave In AS, master In AS, slave  Yes No  Yes  Yes Period Samax. 7  Yes Yes Period Samax. 7  Yes	• Racks, max.	4
Clock  Hardware clock (real-time) retentive and synchronizable Backup time Deviation per day, max. Behavior of the clock following POWER-ON Behavior of the clock following expiry of backup period  Clock continues running after POWER OFF Clock continues running after POWER OFF Clock continues to run with the time at which the power failure occurred  Operating hours counter  Number Number Number 1 Number/Number range Range of values Of to 2^31 hours (when using SFC 101) Granularity retentive  Clock synchronization  Supported OMPI, master OMPI, slave OMPI		8; In rack 3 max. 7
Clock  Hardware clock (real-time) retentive and synchronizable Backup time Deviation per day, max. Behavior of the clock following POWER-ON Behavior of the clock following expiry of backup period  Clock continues running after POWER OFF Clock continues to run with the time at which the power failure occurred  Operating hours counter  Number Number Number 1 Number/Number range 0 Range of values 0 to 2^31 hours (when using SFC 101) Granularity 1 h retentive Yes; Must be restarted at each restart  Clock synchronization  supported Yes to MPI, master To MPI, slave Yes in AS, master in AS, slave  Digital inputs		
Hardware clock (real-time)     retentive and synchronizable     Backup time     Deviation per day, max.     Behavior of the clock following POWER-ON     Behavior of the clock following expiry of backup period  Operating hours counter  Number Number 1 Number/Number range Range of values Range of values Granularity retentive  Clock synchronization  Supported To MPI, master Yes In AS, master In AS, slave  Yes  Yes  Yes  Yes  Yes  Yes  Yes  No  At 40 °C ambient temperature  6 wk; At 40 °C ambient temperature  10 s; Typ.: 2 s  Clock continues running after POWER OFF  Clock continues to run with the time at which the power failure occurred  Operating hours counter  1 Number/Number range 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		
retentive and synchronizable     Backup time     Deviation per day, max.     Behavior of the clock following POWER-ON     Behavior of the clock following expiry of backup period     Operating hours counter      Number     Number		Voo
Backup time Deviation per day, max.  Behavior of the clock following POWER-ON Behavior of the clock following expiry of backup period  Operating hours counter  Number Number Range of values Range of values Granularity retentive  Clock synchronization  Yes; Must be restarted at each restart  OMPI, master OMPI, slave In AS, master In Sk 44 40 °C ambient temperature  6 wk; At 40 °C ambient temperature  10 s; Typ.: 2 s Clock continues running after POWER OFF Clock continues to run with the time at which the power failure occurred  Operating hours counter  1  1  1  1  1  1  1  1  1  1  1  1  1		
<ul> <li>Deviation per day, max.</li> <li>Behavior of the clock following POWER-ON</li> <li>Behavior of the clock following expiry of backup period</li> <li>Operating hours counter</li> <li>Number</li> <li>Number/Number range</li> <li>Range of values</li> <li>Granularity</li> <li>retentive</li> <li>Clock synchronization</li> <li>Supported</li> <li>to MPI, master</li> <li>to MPI, slave</li> <li>in AS, master</li> <li>in AS, slave</li> </ul> <ul> <li>Devaiting POWER OFF</li> <li>Clock continues running after POWER OFF</li> <li>Clock continues to run with the time at which the power failure occurred</li> <li>Clock continues to run with the time at which the power failure occurred</li> <li>Clock continues to run with the time at which the power failure occurred</li> <li>Ot o 2^31 hours (when using SFC 101)</li> <li>1 h</li> <li>Yes; Must be restarted at each restart</li> </ul> Clock synchronization <ul> <li>yes</li> <li>to MPI, slave</li> <li>in AS, master</li> <li>in AS, slave</li> </ul> Digital inputs Digital inputs		
Behavior of the clock following POWER-ON Behavior of the clock following expiry of backup period  Operating hours counter  Number Number		
Behavior of the clock following expiry of backup period  Operating hours counter  Number  Number  Number/Number range  Range of values  Granularity  retentive  Clock synchronization  Supported  Supported  Output  Yes  Output  Yes  Output  Yes  Output  Yes  Output  Number (Number failure occurred)  Output  Ou		
period occurred  Operating hours counter  Number  Number  Number/Number range  Range of values  Granularity  retentive  Clock synchronization  supported  to MPI, master  to MPI, slave  in AS, master  in AS, slave  Occurred  Oc		
Operating hours counter  • Number  • Number/Number range  • Range of values  • Range of values  • Granularity  • retentive  Clock synchronization  • supported  • to MPI, master  • to MPI, slave  • in AS, master  • in AS, slave  Digital inputs		
<ul> <li>Number</li> <li>Number/Number range</li> <li>Range of values</li> <li>O to 2^31 hours (when using SFC 101)</li> <li>Granularity</li> <li>1 h</li> <li>retentive</li> <li>Yes; Must be restarted at each restart</li> </ul> Clock synchronization <ul> <li>supported</li> <li>to MPI, master</li> <li>to MPI, slave</li> <li>in AS, master</li> <li>in AS, master</li> <li>in AS, slave</li> </ul> Digital inputs <ul> <li>No</li> </ul>		Cocurred
<ul> <li>Number/Number range</li> <li>Range of values</li> <li>O to 2^31 hours (when using SFC 101)</li> <li>Granularity</li> <li>retentive</li> <li>Yes; Must be restarted at each restart</li> </ul> Clock synchronization <ul> <li>supported</li> <li>to MPI, master</li> <li>to MPI, slave</li> <li>in AS, master</li> <li>in AS, slave</li> </ul> Digital inputs <ul> <li>No</li> </ul>		1
<ul> <li>Range of values <ul> <li>Granularity</li> <li>retentive</li> </ul> </li> <li>Clock synchronization <ul> <li>supported</li> <li>to MPI, master</li> <li>to MPI, slave</li> <li>in AS, master</li> <li>in AS, slave</li> </ul> </li> <li>Digital inputs</li> </ul> <li>O to 2^31 hours (when using SFC 101)  <ul> <li>1 h</li> <li>Yes; Must be restarted at each restart</li> </ul> </li> <li>Yes <ul> <li>to MPI, slave</li> <li>Yes</li> </ul> </li> <li>No</li>		
<ul> <li>Granularity</li> <li>retentive</li> <li>Yes; Must be restarted at each restart</li> </ul> Clock synchronization <ul> <li>supported</li> <li>to MPI, master</li> <li>to MPI, slave</li> <li>in AS, master</li> <li>in AS, slave</li> </ul> Digital inputs <ul> <li>1 h</li> <li>Yes; Must be restarted at each restart</li> </ul> Yes <ul> <li>to MPI, slave</li> <li>in AS, slave</li> </ul> Digital inputs		
<ul> <li>retentive</li> <li>Yes; Must be restarted at each restart</li> <li>Clock synchronization</li> <li>supported</li> <li>to MPI, master</li> <li>to MPI, slave</li> <li>in AS, master</li> <li>in AS, slave</li> </ul> Digital inputs		
Clock synchronization  • supported • to MPI, master • to MPI, slave • in AS, master • in AS, slave  Digital inputs		
<ul> <li>supported</li> <li>to MPI, master</li> <li>to MPI, slave</li> <li>in AS, master</li> <li>in AS, slave</li> </ul> Digital inputs Yes No		root, must be restarted at odor rootalt
<ul> <li>to MPI, master</li> <li>to MPI, slave</li> <li>in AS, master</li> <li>in AS, slave</li> </ul> Digital inputs Yes No		Yes
<ul> <li>to MPI, slave</li> <li>in AS, master</li> <li>in AS, slave</li> </ul> Digital inputs		
• in AS, master         • in AS, slave          No  Digital inputs  Yes  No		
• in AS, slave  No  Digital inputs		
Digital inputs		
	- III AO, Siave	
Number of digital inputs 24		
	Number of digital inputs	24

<ul> <li>of which inputs usable for technological functions</li> </ul>	16
integrated channels (DI)	24
Input characteristic curve in accordance with IEC 61131, type 1	Yes
Number of simultaneously controllable inputs	
horizontal installation	
— up to 40 °C, max.	24
— up to 60 °C, max.	12
vertical installation	
— up to 40 °C, max.	12
Input voltage	
Rated value (DC)	24 V
• for signal "0"	-3 to +5V
• for signal "1"	+15 to +30 V
Input current	
● for signal "1", typ.	8 mA
Input delay (for rated value of input voltage)	
for standard inputs	
— parameterizable	Yes; 0.1 / 0.3 / 3 / 15 ms (You can reconfigure the input delay of the standard inputs during program runtime. Please note that under certain circumstances your newly set filter time may not be effective until the next filter cycle.)
— Rated value	3 ms
for technological functions	
— at "0" to "1", max.	8 µs; Minimum pulse width/minimum pause between pulses at maximum counting frequency
Cable length	
• shielded, max.	1 000 m; 50 m for technological functions
• unshielded, max.	600 m; for technological functions: No
for technological functions	
— shielded, max.	50 m; at maximum count frequency
— unshielded, max.	not allowed
Digital outputs	
Number of digital outputs	16
of which high-speed outputs	4; Notice: You cannot connect the fast outputs of your CPU in parallel
integrated channels (DO)	16
Short-circuit protection	Yes; Clocked electronically
<ul> <li>Response threshold, typ.</li> </ul>	1 A
Limitation of inductive shutdown voltage to	L+ (-48 V)
Controlling a digital input	Yes
Switching capacity of the outputs	

• on lamp load, max.	5 W
Load resistance range	
• lower limit	48 Ω
• upper limit	4 kΩ
Output voltage	
• for signal "1", min.	L+ (-0.8 V)
Output current	
• for signal "1" rated value	500 mA
• for signal "1" permissible range, min.	5 mA
• for signal "1" permissible range, max.	0.6 A
<ul><li>for signal "1" minimum load current</li></ul>	5 mA
• for signal "0" residual current, max.	0.5 mA
Parallel switching of two outputs	
• for uprating	No
<ul> <li>for redundant control of a load</li> </ul>	Yes
Switching frequency	
• with resistive load, max.	100 Hz
<ul><li>with inductive load, max.</li></ul>	0.5 Hz
• on lamp load, max.	100 Hz
• of the pulse outputs, with resistive load, max.	2.5 kHz
Total current of the outputs (per group)	
horizontal installation	
— up to 40 °C, max.	3 A
— up to 60 °C, max.	2 A
vertical installation	
— up to 40 °C, max.	2 A
Cable length	
• shielded, max.	1 000 m
• unshielded, max.	600 m
Analog inputs	
Number of analog inputs	5
For voltage/current measurement	4
• For resistance/resistance thermometer	1
measurement	
integrated channels (AI)	5; 4x current/voltage, 1x resistance
permissible input voltage for current input	5 V; Permanent
(destruction limit), max.	00 V D
permissible input voltage for voltage input (destruction limit), max.	30 V; Permanent
permissible input current for voltage input	0.5 mA; Permanent
(destruction limit), max.	o.o m. g i omianone
·	

permissible input current for current input (destruction limit), max.	50 mA; Permanent
No-load voltage for resistance-type transmitter, typ.	3.3 V
Constant measurement current for resistance-type transmitter, typ.	1.25 mA
Technical unit for temperature measurement adjustable	Yes; Degrees Celsius / degrees Fahrenheit / Kelvin
Input ranges	
<ul><li>Voltage</li></ul>	Yes; $\pm 10$ V / $100$ k $\Omega$ ; 0 V to 10 V / $100$ k $\Omega$
Current	Yes; ±20 mA / 100 $\Omega$ ; 0 mA to 20 mA / 100 $\Omega$ ; 4 mA to 20 mA / 100 $\Omega$
<ul> <li>Resistance thermometer</li> </ul>	Yes; Pt 100 / 10 MΩ
Resistance	Yes; 0 $\Omega$ to 600 $\Omega$ / 10 M $\Omega$
Input ranges (rated values), voltages	
• 0 to +10 V	Yes
— Input resistance (0 to 10 V)	100 kΩ
Input ranges (rated values), currents	
• 0 to 20 mA	Yes
— Input resistance (0 to 20 mA)	100 Ω
• -20 mA to +20 mA	Yes
— Input resistance (-20 mA to +20 mA)	100 Ω
• 4 mA to 20 mA	Yes
— Input resistance (4 mA to 20 mA)	100 Ω
Input ranges (rated values), resistance thermometer	
• Pt 100	Yes
— Input resistance (Pt 100)	10 MΩ
Input ranges (rated values), resistors	
• 0 to 600 ohms	Yes
— Input resistance (0 to 600 ohms)	10 ΜΩ
Thermocouple (TC)	
Temperature compensation	
— parameterizable	No
Characteristic linearization	
parameterizable	Yes; by software
— for resistance thermometer	Pt 100
Cable length	
• shielded, max.	100 m
Analog outputs	
Number of analog outputs	2
integrated channels (AO)	2
Voltage output, short-circuit protection	Yes
Voltage output, short-circuit current, max.	55 mA

Current output, no-load voltage, max.	14 V
Output ranges, voltage	
• 0 to 10 V	Yes
• -10 V to +10 V	Yes
Output ranges, current	
• 0 to 20 mA	Yes
• -20 mA to +20 mA	Yes
• 4 mA to 20 mA	Yes
Connection of actuators	
for voltage output two-wire connection	Yes; Without compensation of the line resistances
for voltage output four-wire connection	No
for current output two-wire connection	Yes
Load impedance (in rated range of output)	
with voltage outputs, min.	1 kΩ
<ul> <li>with voltage outputs, capacitive load, max.</li> </ul>	0.1 μF
with current outputs, max.	300 Ω
with current outputs, inductive load, max.	0.1 mH
Destruction limits against externally applied voltages an	d currents
Voltages at the outputs towards MANA	16 V; Permanent
• Current, max.	50 mA; Permanent
Cable length	
• shielded, max.	200 m
	200 m
Analog value generation for the inputs	
	200 m  Actual value encryption (successive approximation)
Analog value generation for the inputs  Measurement principle	
Analog value generation for the inputs  Measurement principle  Integration and conversion time/resolution per channel	Actual value encryption (successive approximation)
Analog value generation for the inputs  Measurement principle  Integration and conversion time/resolution per channel  • Resolution with overrange (bit including sign),	Actual value encryption (successive approximation)
Analog value generation for the inputs  Measurement principle  Integration and conversion time/resolution per channel  • Resolution with overrange (bit including sign), max.	Actual value encryption (successive approximation)  12 bit
Analog value generation for the inputs  Measurement principle  Integration and conversion time/resolution per channel  • Resolution with overrange (bit including sign), max.  • Integration time, parameterizable	Actual value encryption (successive approximation)  12 bit  Yes; 16.6 / 20 ms
Analog value generation for the inputs  Measurement principle  Integration and conversion time/resolution per channel  • Resolution with overrange (bit including sign), max.  • Integration time, parameterizable  • Interference voltage suppression for	Actual value encryption (successive approximation)  12 bit  Yes; 16.6 / 20 ms
Analog value generation for the inputs  Measurement principle  Integration and conversion time/resolution per channel  • Resolution with overrange (bit including sign), max.  • Integration time, parameterizable  • Interference voltage suppression for interference frequency f1 in Hz	Actual value encryption (successive approximation)  12 bit  Yes; 16.6 / 20 ms 50 / 60 Hz
Analog value generation for the inputs  Measurement principle  Integration and conversion time/resolution per channel  • Resolution with overrange (bit including sign), max.  • Integration time, parameterizable  • Interference voltage suppression for interference frequency f1 in Hz  • permissible input frequency, max.  • Time constant of the input filter  • Basic execution time of the module (all	Actual value encryption (successive approximation)  12 bit  Yes; 16.6 / 20 ms  50 / 60 Hz  400 Hz
Analog value generation for the inputs  Measurement principle  Integration and conversion time/resolution per channel  • Resolution with overrange (bit including sign), max.  • Integration time, parameterizable  • Interference voltage suppression for interference frequency f1 in Hz  • permissible input frequency, max.  • Time constant of the input filter	Actual value encryption (successive approximation)  12 bit  Yes; 16.6 / 20 ms  50 / 60 Hz  400 Hz  0.38 ms
Analog value generation for the inputs  Measurement principle  Integration and conversion time/resolution per channel  • Resolution with overrange (bit including sign), max.  • Integration time, parameterizable  • Interference voltage suppression for interference frequency f1 in Hz  • permissible input frequency, max.  • Time constant of the input filter  • Basic execution time of the module (all	Actual value encryption (successive approximation)  12 bit  Yes; 16.6 / 20 ms  50 / 60 Hz  400 Hz  0.38 ms
Analog value generation for the inputs  Measurement principle  Integration and conversion time/resolution per channel  • Resolution with overrange (bit including sign), max.  • Integration time, parameterizable  • Interference voltage suppression for interference frequency f1 in Hz  • permissible input frequency, max.  • Time constant of the input filter  • Basic execution time of the module (all channels released)	Actual value encryption (successive approximation)  12 bit  Yes; 16.6 / 20 ms  50 / 60 Hz  400 Hz  0.38 ms
Analog value generation for the inputs  Measurement principle  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  permissible input frequency, max.  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs	Actual value encryption (successive approximation)  12 bit  Yes; 16.6 / 20 ms  50 / 60 Hz  400 Hz  0.38 ms
Analog value generation for the inputs  Measurement principle  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  permissible input frequency, max.  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel	Actual value encryption (successive approximation)  12 bit  Yes; 16.6 / 20 ms  50 / 60 Hz  400 Hz  0.38 ms  1 ms
Analog value generation for the inputs  Measurement principle  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  permissible input frequency, max.  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)	Actual value encryption (successive approximation)  12 bit  Yes; 16.6 / 20 ms  50 / 60 Hz  400 Hz  0.38 ms  1 ms
Analog value generation for the inputs  Measurement principle  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  permissible input frequency, max.  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time	Actual value encryption (successive approximation)  12 bit  Yes; 16.6 / 20 ms 50 / 60 Hz  400 Hz 0.38 ms 1 ms
Analog value generation for the inputs  Measurement principle  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  permissible input frequency, max.  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)	Actual value encryption (successive approximation)  12 bit  Yes; 16.6 / 20 ms 50 / 60 Hz  400 Hz 0.38 ms 1 ms

<ul> <li>for inductive load</li> </ul>	0.5 ms
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Encoder	
Connection of signal encoders	
for voltage measurement	Yes
• for current measurement as 2-wire transducer	Yes; with external supply
• for current measurement as 4-wire transducer	Yes
<ul> <li>for resistance measurement with two-wire connection</li> </ul>	Yes; Without compensation of the line resistances
<ul> <li>for resistance measurement with three-wire connection</li> </ul>	No
<ul> <li>for resistance measurement with four-wire connection</li> </ul>	No
Connectable encoders	
• 2-wire sensor	Yes
<ul> <li>permissible quiescent current (2-wire sensor), max.</li> </ul>	1.5 mA
Errors/accuracies	
Temperature error (relative to input range), (+/-)	0.006 %/K
Crosstalk between the inputs, min.	60 dB
Repeat accuracy in steady state at 25 °C (relative to	0.06 %
input range), (+/-)	
Output ripple (relative to output range, bandwidth 0 to 50 kHz), (+/-)	0.1 %
Linearity error (relative to output range), (+/-)	0.15 %
Temperature error (relative to output range), (+/-)	0.01 %/K
Crosstalk between the outputs, min.	60 dB
Repeat accuracy in steady state at 25 °C (relative to output range), (+/-)	0.06 %
Operational error limit in overall temperature range	
<ul> <li>Voltage, relative to input range, (+/-)</li> </ul>	1 %
<ul> <li>Current, relative to input range, (+/-)</li> </ul>	1 %
<ul> <li>Resistance, relative to input range, (+/-)</li> </ul>	1 %
<ul> <li>Voltage, relative to output range, (+/-)</li> </ul>	1 %
<ul><li>Current, relative to output range, (+/-)</li></ul>	1 %
Basic error limit (operational limit at 25 °C)	
<ul> <li>Voltage, relative to input range, (+/-)</li> </ul>	0.8 %; Linearity error ±0.06 %
<ul> <li>Current, relative to input range, (+/-)</li> </ul>	0.8 %; Linearity error ±0.06 %
• Resistance, relative to input range, (+/-)	0.8 %; Linearity error ±0.2 %
<ul> <li>Resistance thermometer, relative to input range, (+/-)</li> </ul>	0.8 %
<ul> <li>Voltage, relative to output range, (+/-)</li> </ul>	0.8 %
• Current, relative to output range, (+/-)	0.8 %
Interference voltage suppression for f = n x (f1 +/- 1 %),	f1 = interference frequency

<ul> <li>Series mode interference (peak value of interference &lt; rated value of input range), min.</li> </ul>	30 dB
• Common mode interference, min.	40 dB
Interfaces	
Number of industrial Ethernet interfaces	0
Number of PROFINET interfaces	0
Number of RS 485 interfaces	1; MPI
Number of RS 422 interfaces	1; RS 422 / 485 combined
Point-to-point connection	
Cable length, max.	1 200 m
Integrated protocol driver	
— 3964 (R)	Yes
— ASCII	Yes
— RK 512	Yes
Transmission rate, RS 422/485	
— with 3964 (R) protocol, max.	19.2 kbit/s; 38.4 kbit/s half duplex; 19.2 kbit/s full duplex
— with ASCII protocol, max.	19.2 kbit/s; 38.4 kbit/s half duplex; 19.2 kbit/s full duplex
— with RK 512 protocol, max.	19.2 kbit/s; 38.4 kbit/s half duplex; 19.2 kbit/s full duplex
1. Interface	
Interface type	Integrated RS 485 interface
Isolated	No
Power supply to interface (15 to 30 V DC), max.	200 mA
Interface types	
• RS 485	Yes
Protocols	
• MPI	Yes
PROFIBUS DP master	No
<ul> <li>PROFIBUS DP slave</li> </ul>	No
<ul> <li>Point-to-point connection</li> </ul>	No
MPI	
<ul><li>Transmission rate, max.</li></ul>	187.5 kbit/s
Services	
— PG/OP communication	Yes
— Routing	No
<ul> <li>Global data communication</li> </ul>	Yes
<ul> <li>— S7 basic communication</li> </ul>	Yes
— S7 communication	Yes; Only server, configured on one side
<ul> <li>— S7 communication, as client</li> </ul>	No; but via CP and loadable FB
— S7 communication, as server	Yes
2. Interface	
Interface type	Integrated RS 422/ 485 interface

Isolated	Yes
Power supply to interface (15 to 30 V DC), max.	No
Interface types	
• RS 485	Yes; RS 422 / 485 (X.27)
Protocols	
• MPI	No
PROFINET IO Controller	No
PROFINET IO Device	No
PROFINET CBA	No
PROFIBUS DP master	No
PROFIBUS DP slave	No
Point-to-point connection	Yes
Point-to-point connection	
Transmission rate, max.	19.2 kbit/s; 38.4 kbit/s half duplex; 19.2 kbit/s full duplex
• Interface controllable from the user program	Yes
• Interface can trigger alarm/interrupt in the user	Yes; Message on break - identification
program	
Communication functions	
PG/OP communication	Yes
Data record routing	No
Global data communication	
• supported	Yes
<ul><li>Number of GD loops, max.</li></ul>	8
<ul> <li>Number of GD packets, max.</li> </ul>	8
<ul> <li>Number of GD packets, transmitter, max.</li> </ul>	8
<ul> <li>Number of GD packets, receiver, max.</li> </ul>	8
<ul> <li>Size of GD packets, max.</li> </ul>	22 byte
• Size of GD packet (of which consistent), max.	22 byte
S7 basic communication	
• supported	Yes
<ul> <li>User data per job, max.</li> </ul>	76 byte
<ul> <li>User data per job (of which consistent), max.</li> </ul>	76 byte; 76 bytes (with X_SEND or X_RCV); 64 bytes (with X_PUT or X_GET as server)
S7 communication	
• supported	Yes
• as server	Yes
• as client	Yes; Via CP and loadable FB
<ul> <li>User data per job, max.</li> </ul>	180 kbyte; With PUT/GET
• User data per job (of which consistent), max.	240 byte; as server
S5 compatible communication	
• supported	Yes; via CP and loadable FC
Number of connections	

• overall	12
<ul> <li>usable for PG communication</li> </ul>	11
<ul> <li>reserved for PG communication</li> </ul>	1
— adjustable for PG communication, min.	1
— adjustable for PG communication, max.	11
<ul> <li>usable for OP communication</li> </ul>	11
<ul> <li>reserved for OP communication</li> </ul>	1
— adjustable for OP communication, min.	1
— adjustable for OP communication, max.	11
<ul> <li>usable for S7 basic communication</li> </ul>	8
<ul> <li>reserved for S7 basic communication</li> </ul>	0
<ul> <li>adjustable for S7 basic communication,</li> </ul>	0
min.	
<ul> <li>adjustable for S7 basic communication,</li> </ul>	8
max.	

S7 message functions	
Number of login stations for message functions, max.	12; Depending on the configured connections for PG/OP and S7
	basic communication
Process diagnostic messages	Yes
simultaneously active Alarm-S blocks, max.	300

Test commissioning functions		
Status block	Yes; Up to 2 simultaneously	
Single step	Yes	
Number of breakpoints	4	
Status/control		
Status/control variable	Yes	
<ul> <li>Variables</li> </ul>	Inputs, outputs, memory bits, DB, times, counters	
<ul> <li>Number of variables, max.</li> </ul>	30	
— of which status variables, max.	30	
— of which control variables, max.	14	
Forcing		
• Forcing	Yes	
• Forcing, variables	Inputs, outputs	
<ul> <li>Number of variables, max.</li> </ul>	10	
Diagnostic buffer		
• present	Yes	
<ul> <li>Number of entries, max.</li> </ul>	500	
— adjustable	No	
— of which powerfail-proof	100; Only the last 100 entries are retained	
<ul> <li>Number of entries readable in RUN, max.</li> </ul>	499	
— adjustable	Yes; From 10 to 499	

propet	10	
— preset  Service data	10	
• can be read out	Yes	
Can be read out	163	
Interrupts/diagnostics/status information		
Diagnostics indication LED		
<ul> <li>Status indicator digital input (green)</li> </ul>	Yes	
<ul> <li>Status indicator digital output (green)</li> </ul>	Yes	
Integrated Functions		
Number of counters	4; See "Technological Functions" manual	
Counting frequency (counter) max.	60 kHz	
Frequency measurement	Yes	
Number of frequency meters	4; up to 60 kHz (see "Technological Functions" manual)	
controlled positioning	Yes	
integrated function blocks (closed-loop control)	Yes; PID controller (see "Technological Functions" manual)	
PID controller	Yes	
Number of pulse outputs	4; Pulse width modulation up to 2.5 kHz (see "Technological Functions" Manual)	
Limit frequency (pulse)	2.5 kHz	
Potential separation		
Potential separation digital inputs		
Potential separation digital inputs	Yes	
• between the channels	No	
<ul> <li>between the channels and backplane bus</li> </ul>	Yes	
Potential separation digital outputs		
Potential separation digital outputs	Yes	
• between the channels	Yes	
<ul> <li>between the channels, in groups of</li> </ul>	8	
between the channels and backplane bus	Yes	
Potential separation analog inputs		
Potential separation analog inputs	Yes; common for analog I/O	
between the channels	No	
between the channels and backplane bus	Yes	
Potential separation analog outputs		
Potential separation analog outputs	Yes; common for analog I/O	
• between the channels	No	
between the channels and backplane bus	Yes	
Isolation		
Isolation tested with	600 V DC	
Ambient conditions		
Ambient temperature during operation		

● min.	0 °C
• max.	60 °C

Configuration		
Configuration software		
• STEP 7	Yes; STEP 7 V5.5 + SP1 or higher or STEP 7 V5.3 + SP2 or	
	higher with HSP 203	
• STEP 7 Lite	No	
Programming		
Command set	see instruction list	
<ul> <li>Nesting levels</li> </ul>	8	
<ul> <li>System functions (SFC)</li> </ul>	see instruction list	
<ul> <li>System function blocks (SFB)</li> </ul>	see instruction list	
Programming language		
— LAD	Yes	
— FBD	Yes	
— STL	Yes	
— SCL	Yes	
— CFC	Yes	
— GRAPH	Yes	
— HiGraph®	Yes	
Know-how protection		
User program protection/password protection	Yes	
<ul> <li>Block encryption</li> </ul>	Yes; With S7 block Privacy	
Dimensions		
Width	120 mm	
Height	125 mm	
Depth	130 mm	
Weights		
Weight, approx.	680 g	

12/10/2020

last modified: